

① Programming Counter

Counter instruction

A Programmed Counter can perform the same function as a mechanical counter. A counter acts similarly to a timer except it does not operate on an external clock but depends on external program sources for counting.

* It is included as part of the counter instruction is the counter preset value & the current accumulated count for the counter.

* PLC counter can be designed to count up to a preset value (or) to count down to a preset value.

* The up-counter is incremented by 1 each time the rung containing the counter is energized.

* The down-counter is decremented by 1 each time the rung containing the counter is energized.

* The value indicated by the counter is termed as the accumulated value.

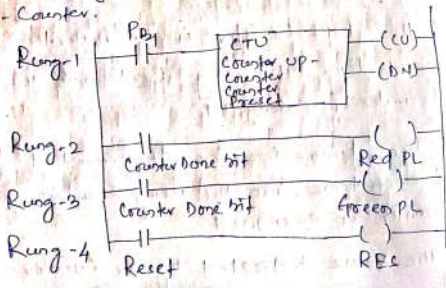
Types of PLC Counter ⁽²⁾

In general, a PLC Counter can be classified into 3 types. These are:

- Up-Counter
- Down-Counter
- Up-down Counter

Up-Counter

The Count-up Counter is an O/P instrⁿ whose function is to increment its accumulated value on false-to-true transition of its instrⁿ. The up-counter O/P instrⁿ will increment by 1 each time, the counted event occurs. Fig shows the program and timing diagram for a simple up-counter.



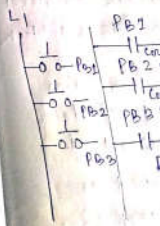
Down-Counter

The instrⁿ will count by 1 each time it occurs.

* Each time the value occurs, it is decremented.

* Normally the instrⁿ is in conjunction with an UP/DN

* Fig shows the for a UP/DN



of Cnt - Preset

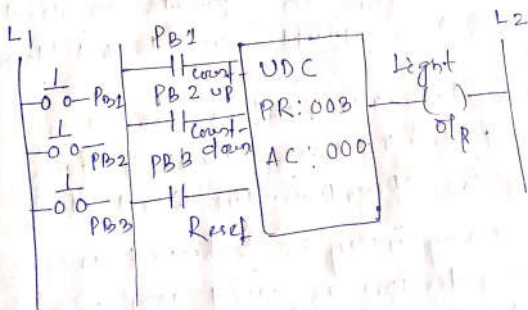
Down-counter:- (3)

The down-counter ofp. insf will Count down by decrement by 1 each time the Counted event is occurs.

* Each time the down Count event value occurs, the accumulated value is decremented.

* Normally the down-counter is used in conjunction with the up-counter to form an Up/down Counter.

* Fig shows the Program & timing diagram for a up/down counter.



Data Manipulation⁽⁴⁾ Instruction of PLC

Data Manipulation:-

Data Manipulation instⁿ enable the Programmable Controller to take some of the qualities of a computer system.

- * A Data Manipulation can involves to transfer of data & operation on data with math functions, data conversion, data comparison & logical operation.
- * There are two basic instⁿ, the instruction that can operate on word data & those operate on file or block.
- * Each data manipulation instruction required two (or) more words of data memory for operation. The word of data memory may be returned as either register (or) as word that can depend upon manufacturer.

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* The data Manipulation insⁿ can allow the movement, Manipulation and storage of data in either single or multiple - word groups from one data memory area to another.

* A Data Manipulation can be placed in two categories
(a) Data transfer.
(b) Data Comparison.

Data transfer operation:-

A 'Data transfer' insⁿ can involve the transfer of the contents from one word or register to another.

* A Data transfer insⁿ can address any location in the memory. Stored values can be retrieved & they can be placed in any new location. They can be preset register for a timer or counter or even an o/p register.

* In basically there are two types of insⁿ set they are used to acquire & move data.

The Allen-Bradley PLC-2 Controller can use ctrl - formatted

(6)
for data transfer instⁿ. These are
GET & PUT.

* GET instruction tell the processor
to go get a value stored in some
word. The GET instruction is programmed
in the Condⁿ portion of a logic rung.

* Similarly PUT instruction tell the
processor where the PUT instruction
is obtained from the GET instⁿ. The
PUT instⁿ is programmed in the
Op portion of the Logic Rung. A PUT
instⁿ receives all 16-bit of data from
processing the GET instⁿ.

Data Compare instⁿ -

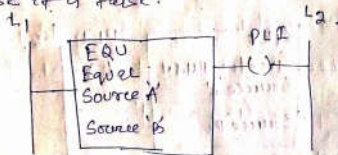
As we know that a data
transfer operations are all op instⁿ
where as the data compare instⁿ
are input instⁿ.

* A data compare instⁿ can Comp
the data stored in two or more
word or register & also make a decision.

based on the Program instruction.

* The Numeric values in two words of memory can be compared for each of the following condⁿ. Itself can depend on the PLC. These are:

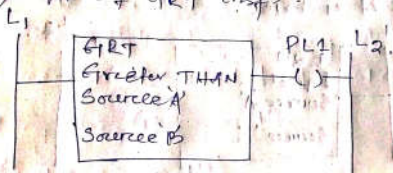
(a) Equal instruction (EQU) :- The equal instrⁿ is an instrⁿ itself can compare source 'A' to source 'B'. Where source 'A' is equal to source 'B'. Then this instrⁿ is logically true. otherwise it is false.



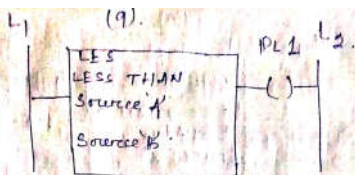
(b) Not Equal (NEQ) instruction :- The not equal (NEQ) instrⁿ is an opp. instrⁿ itself can compare source 'A' to source 'B'. Where source 'A' is not equal to source 'B'. Then the instrⁿ is logically true. otherwise it is logically false. fig shows an example of the NEQ instrⁿ.



(3) Greater than (GRT) insf:- The Greater than insf is an input insf. It can compare Source A to Source B. Where Source A is greater than Source B, then the insf is logically true otherwise it is logically false. Fig shows an example of GRT insf.

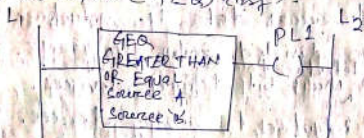


(4) Less than (LES) insf:- The less than insf is an input insf. It can compare Source A to Source B. Where Source A is less than Source B, then the insf is logically true otherwise it is logically false. Fig shows an example of less than insf.



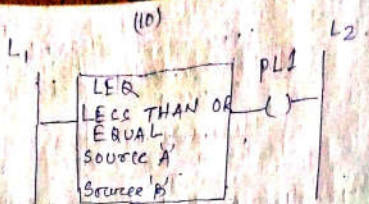
5) Greater than or equal (GEQ) instⁿ:-

This instⁿ is an instⁿ instruction that can compare source A to source B. When source A is greater than (or) equal to source B, this instruction is logically true otherwise it is logically false. Fig shows an example of Greater than or equal (GEQ) instⁿ.



6) Less than (or) equal (LEQ) instⁿ:-

This instruction is an instⁿ instruction that can compare source A to source B. When source A is Less than (or) equal to source B, this instruction is logically true otherwise it is logically false. Fig shows an example of the LEQ instⁿ instruction.



Numerical data I/O interfaces.

The capabilities of data Manipulation Processing of PLC which can lead to a I/O interfaces is known as numerical data I/O interfaces.

* Numerical data I/O interfaces can be divided into two groups.

- (a) ^{Interface to} Multi bit digital devices.
- (b) Interface to analog devices.

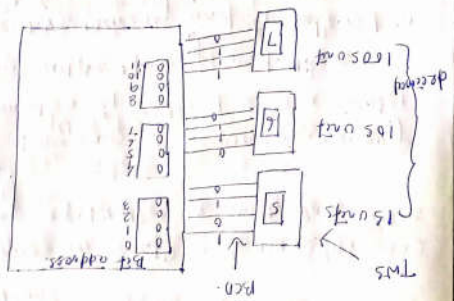
* The multi bit digital devices are called discrete I/O devices.

Because processed signals are discrete (ON/OFF).

* In discrete I/O a single bit is required to read an input or control an output.

* The new interface allows a group of up to 16 I/O chips.

* It also stores a bus input interface as a unit.
module connected to bus interface switch (TMS).



The bus input module can be used to accept 4-bit digital codes. This module input data into specific register (or word location) in memory to be used by the computer program. Register input modules accept generally a voltage in the range of 5V d.c. to 24V d.c. Data translation function are used to access the data

(12)
from the module that can provide data
to register used in the control program.

SEQUENCER INSTRUCTION!

Sequencer instⁿ are
used to control automatic assembly
M/c that have a consistent & repeatable
operation.

* Sequencer instⁿ can make program
Many application a much ~~easy~~ easier
task.

* To program, a sequence, binary
information is entered into a series
of consecutive memory word. These
consecutive memory word is returned on
a word file. First of all a data can
entered into a word file for each
sequencer step. Through the steps,
binary information is transferred from
the word file to the output word.

* The Sequencer op (SQO) instⁿ can be
used to control o/p devices sequentially.
The sequence of operation is stored
in a data file & this information is
transferred sequentially to the o/p.